

L Number	Hits	Search Text	DB	Time stamp
1	1004	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator	USPAT; EPO; JPO; DERWENT	2004/06/16 12:23
2	352	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1	USPAT; EPO; JPO; DERWENT	2004/06/16 12:24
3	122	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1)	USPAT; EPO; JPO; DERWENT	2004/06/16 12:25
4	42	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)	USPAT; EPO; JPO; DERWENT	2004/06/16 12:25
5	10	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) same polynomial\$1	USPAT; EPO; JPO; DERWENT	2004/06/16 12:27
6	23	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1	USPAT; EPO; JPO; DERWENT	2004/06/16 12:27
7	19	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1 and processor\$1	USPAT; EPO; JPO; DERWENT	2004/06/16 12:39
11	0	(714/781.ccls.) and ((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1 and processor\$1 and (configur\$5 or program\$5) and dynamic\$4	USPAT; EPO; JPO; DERWENT	2004/06/16 12:30
9	11	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1 and processor\$1 and (configur\$5 or program\$5) and dynamic\$4	USPAT; EPO; JPO; DERWENT	2004/06/16 12:30
10	6	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same (shift adj3 register\$1) same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1 and processor\$1 and (configur\$5 or program\$5) and dynamic\$4	USPAT; EPO; JPO; DERWENT	2004/06/16 12:30
8	19	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same register\$1 same (multiplexer\$1 or MUX\$1 or selector\$1) same ("XOR" or logic)) and polynomial\$1 and processor\$1 and (configur\$5 or program\$5)	USPAT; EPO; JPO; DERWENT	2004/06/16 12:31
13	8	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) and (shift adj2 register\$1) and (multiplexer\$1 or MUX\$1 or selector\$1) and ("XOR" or logic)) and polynomial\$1 and processor\$1 and (dynamic\$4 near3 (configur\$4 or program\$5))	USPAT; EPO; JPO; DERWENT	2004/06/16 12:40
12	96	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) and (shift adj2 register\$1) and (multiplexer\$1 or MUX\$1 or selector\$1) and ("XOR" or logic)) and polynomial\$1 and processor\$1	USPAT; EPO; JPO; DERWENT	2004/06/16 13:20
14	1	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same (pre-select\$4 near3 polynomial\$1)	USPAT; EPO; JPO; DERWENT	2004/06/16 13:23

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15	2	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) and (pre-select\$4 near3 polynomial\$1)	USPAT; EPO; JPO; DERWENT	2004/06/16 13:27
16	10	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) same (N-bit)	USPAT; EPO; JPO; DERWENT	2004/06/16 13:27
17	59	((CRC or (cyclic adj2 redundancy adj2 check)) adj3 generator) and (N-bit)	USPAT; EPO; JPO; DERWENT	2004/06/16 13:34

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Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard1 **Parallel CRC generation***Albertengo, G.; Sisto, R.;*

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[\[Abstract\]](#) [\[PDF Full-Text \(916 KB\)\]](#) **IEEE JNL**2 **A new parallel algorithm for CRC generation***Joshi, S.M.; Dubey, P.K.; Kaplan, M.A.;*

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